6ES7314-6BH04-0AB0

Data sheet



SIMATIC S7-300, CPU 314C-2 PTP Compact CPU with MPI, 24 DI/16 DO, 4 AI, 2 AO, 1 Pt100, 4 high-speed counters (60 kHz), integrated interface RS485, Integr. power supply 24 V DC, work memory 192 KB, Front connector (2x 40-pole) and Micro Memory Card required

General information	
HW functional status	01
Firmware version	V3.3
Engineering with	
Programming package	STEP 7 as of V5.5 + SP1 or STEP 7 V5.3 + SP2 or higher with HSP 204
Supply voltage	
Rated value (DC)	24 V
permissible range, lower limit (DC)	19.2 V
permissible range, upper limit (DC)	28.8 V
external protection for power supply lines (recommendation)	Miniature circuit breaker, type C; min. 2 A; miniature circuit breaker type B, min. 4 A
Mains buffering	
 Mains/voltage failure stored energy time 	5 ms
Repeat rate, min.	1 s
Load voltage L+	
Digital inputs	
— load voltage / at digital input / at DC / rated value	24 V
 Reverse polarity protection 	Yes
Digital outputs	
— Rated value (DC)	24 V
 Reverse polarity protection 	No
Input current	
Current consumption (rated value)	660 mA
Current consumption (in no-load operation), typ.	150 mA
Inrush current, typ.	5 A
l²t	0.7 A ² ·s
Digital inputs	
 from load voltage L+ (without load), max. 	80 mA
Digital outputs	
• from load voltage L+, max.	50 mA
Power loss	
Power loss, typ.	13 W
Memory	
Work memory	
• integrated	192 kbyte
expandable	No
Load memory	
• Plug-in (MMC)	Yes
Plug-in (MMC), max.	8 Mbyte
 Data management on MMC (after last programming), min. 	10 a

Backup	
• present	Yes; Guaranteed by MMC (maintenance-free)
without battery	Yes; Program and data
CPU processing times	
for bit operations, typ.	0.06 µs
for word operations, typ.	0.12 µs
for fixed point arithmetic, typ.	0.16 µs
for floating point arithmetic, typ.	0.59 µs
CPU-blocks	
Number of blocks (total)	1 024; (DBs, FCs, FBs); the maximum number of loadable blocks can be reduced by the MMC used.
DB	,
Number, max.	1 024; Number range: 1 to 16000
• Size, max.	64 kbyte
FB	
Number, max.	1 024; Number range: 0 to 7999
• Size, max.	64 kbyte
FC	
Number, max.	1 024; Number range: 0 to 7999
• Size, max.	64 kbyte
OB	
 Number, max. 	see instruction list
• Size, max.	64 kbyte
 Number of free cycle OBs 	1; OB 1
 Number of time alarm OBs 	1; OB 10
 Number of delay alarm OBs 	2; OB 20, 21
 Number of cyclic interrupt OBs 	4; OB 32, 33, 34, 35
 Number of process alarm OBs 	1; OB 40
 Number of startup OBs 	1; OB 100
 Number of asynchronous error OBs 	4; OB 80, 82, 85, 87
Number of synchronous error OBs	2; OB 121, 122
Nesting depth	
per priority class	16
additional within an error OB	4
Counters, timers and their retentivity	
S7 counter	
Number	256
Retentivity	
— adjustable	Yes
— preset	Z 0 to Z 7
Counting range	
— lower limit	0
— upper limit	999
IEC counter	Von
• present	Yes
• Type	SFB Unlimited (limited only by PAM canacity)
Number S7 times	Unlimited (limited only by RAM capacity)
• Number	256
Retentivity	200
— adjustable	Yes
— adjustable — preset	No retentivity
— preser Time range	No foliativity
— lower limit	10 ms
— upper limit — upper limit	9 990 s
— upper illrilit	0 000 3
• present	Yes
	SFB
Type Number	Unlimited (limited only by RAM capacity)
	offillification by RAIVI capacity)
Data areas and their retentivity Retentive data area (incl. timers, counters, flags), max.	64 kbyte

Flog	
Flag ◆ Size, max.	256 byte
Retentivity available	Yes; MB 0 to MB 255
•	
Retentivity preset Number of clearly represents.	MB 0 to MB 15
Number of clock memories Pata blacks	8; 1 memory byte
Data blocks	Versite are estate according DD
Retentivity adjustable	Yes; via non-retain property on DB
Retentivity preset	Yes
Local data	
per priority class, max.	32 kbyte; Max. 2048 bytes per block
Address area	
I/O address area	
• Inputs	1 024 byte
Outputs	1 024 byte
of which distributed	
— Inputs	none
— Outputs	none
Process image	
• Inputs	1 024 byte
Outputs	1 024 byte
 Inputs, adjustable 	1 024 byte
Outputs, adjustable	1 024 byte
 Inputs, default 	128 byte
Outputs, default	128 byte
Default addresses of the integrated channels	
— Digital inputs	124.0 to 126.7
— Digital outputs	124.0 to 125.7
— Analog inputs	752 to 761
 Analog outputs 	752 to 755
Digital channels	
• Inputs	1 016
— of which central	1 016
Outputs	1 008
— of which central	1 008
Analog channels	
• Inputs	253
— of which central	253
Outputs	250
— of which central	250
Hardware configuration	
Number of expansion units, max.	3
Number of DP masters	
• integrated	none
• via CP	4
	4
Number of operable FMs and CPs (recommended)	0
• FM	8
• CP, PtP	8
• CP, LAN	10
Rack	
• Racks, max.	4
Modules per rack, max.	8; In rack 3 max. 7
Time of day	
Clock	
 Hardware clock (real-time) 	Yes
 retentive and synchronizable 	Yes
Backup time	6 wk; At 40 °C ambient temperature
 Deviation per day, max. 	10 s; Typ.: 2 s
 Behavior of the clock following POWER-ON 	Clock continues running after POWER OFF
 Behavior of the clock following expiry of backup period 	the clock continues at the time of day it had when power was switched off
Operating hours counter	

1 0 0 to 2^31 hours (when using SFC 101) 1 h Yes; Must be restarted at each restart
0 to 2^31 hours (when using SFC 101) 1 h
1 h
Yes; Must be restarted at each restart
Yes
Yes
Yes
Yes
No
24
16
24
Yes
24
12
12
24 V
-3 to +5V
+15 to +30 V
8 mA
Yes; 0.1 / 0.3 / 3 / 15 ms (You can reconfigure the input delay of the standard inputs during program runtime. Please note that under certain circumstances your newly set filter time may not be effective until the next filter cycle.)
3 ms
8 µs; Minimum pulse width/minimum pause between pulses at maximum counting frequency
1 000 m; 50 m for technological functions
600 m; for technological functions: No
50 m; at maximum count frequency
not allowed
16
4; Notice: You cannot connect the fast outputs of your CPU in parallel
16
Yes; Clocked electronically
1 A
L+ (-48 V)
Yes
5 W
48 Ω
4 kΩ
L+ (-0.8 V)
500 mA
5 mA
YN 212Y 21 1 2 + 8 YiiY3 8c 16 5 n 141Y1LY 5 44 L 5

for circul 11411 pormissible reason may	0.0 A
• for signal "1" permissible range, max.	0.6 A
for signal "1" minimum load current for signal "0" residual current, required	5 mA
for signal "0" residual current, max.	0.5 mA
Parallel switching of two outputs	No
for upratingfor redundant control of a load	Yes
Switching frequency	165
with resistive load, max.	100 Hz
with inductive load, max.	0.5 Hz
on lamp load, max.	100 Hz
of the pulse outputs, with resistive load, max.	2.5 kHz
Total current of the outputs (per group)	2.5 N IZ
horizontal installation	
— up to 40 °C, max.	3 A
— up to 60 °C, max.	2 A
vertical installation	
— up to 40 °C, max.	2 A
Cable length	
shielded, max.	1 000 m
• unshielded, max.	600 m
Analog inputs	
Number of analog inputs	5
For voltage/current measurement	4
For resistance/resistance thermometer measurement	1
integrated channels (AI)	5; 4x current/voltage, 1x resistance
permissible input voltage for current input (destruction limit), max.	5 V; Permanent
permissible input voltage for voltage input (destruction limit), max.	30 V; Permanent
permissible input current for voltage input (destruction limit), max.	0.5 mA; Permanent
permissible input current for current input (destruction limit), max.	50 mA; Permanent
Electrical input frequency, max.	400 Hz
No-load voltage for resistance-type transmitter, typ.	3.3 V
Constant measurement current for resistance-type transmitter, typ.	1.25 mA
Technical unit for temperature measurement adjustable	Yes; Degrees Celsius / degrees Fahrenheit / Kelvin
Input ranges	
 Voltage 	Yes; ± 10 V / 100 k Ω ; 0 V to 10 V / 100 k Ω
Current	Yes; ±20 mA / 100 Ω ; 0 mA to 20 mA / 100 Ω ; 4 mA to 20 mA / 100 Ω
Resistance thermometer	Yes; Pt 100 / 10 MΩ
Resistance	Yes; 0 Ω to 600 Ω / 10 M Ω
Input ranges (rated values), voltages	
• 0 to +10 V	Yes
— Input resistance (0 to 10 V)	100 kΩ
Input ranges (rated values), currents	N/
• 0 to 20 mA	Yes
— Input resistance (0 to 20 mA)	100 Ω
• -20 mA to +20 mA	Yes
— Input resistance (-20 mA to +20 mA)	100 Ω
• 4 mA to 20 mA	Yes
— Input resistance (4 mA to 20 mA)	100 Ω
Input ranges (rated values), resistance thermometer • Pt 100	Yes
— Input resistance (Pt 100)	10 MΩ
Input ranges (rated values), resistors	10 M77
• 0 to 600 ohms	Yes
Input resistance (0 to 600 ohms)	10 MΩ
Thermocouple (TC)	10 11122
Temperature compensation	
— parameterizable	No
paramotonzable	

Characteristic linearization	V
parameterizable	Yes; by software
— for resistance thermometer	Pt 100
Cable length	
• shielded, max.	100 m
Analog outputs	
Number of analog outputs	2
integrated channels (AO)	2
Voltage output, short-circuit protection	Yes
Voltage output, short-circuit current, max.	55 mA
Current output, no-load voltage, max.	14 V
Output ranges, voltage	
• 0 to 10 V	Yes
• -10 V to +10 V	Yes
Output ranges, current	
• 0 to 20 mA	Yes
• -20 mA to +20 mA	Yes
• 4 mA to 20 mA	Yes
Connection of actuators	
for voltage output two-wire connection	Yes; Without compensation of the line resistances
for voltage output four-wire connection	No
for current output two-wire connection	Yes
Load impedance (in rated range of output)	
with voltage outputs, min.	1 kΩ
with voltage outputs, min. with voltage outputs, capacitive load, max.	0.1 μF
 with voltage outputs, capacitive load, max. with current outputs, max. 	300 Ω
-	
with current outputs, inductive load, max. Posturation limits against externally applied voltages and surrents.	0.1 mH
Destruction limits against externally applied voltages and currents	40.1/2 Damaga and
Voltages at the outputs towards MANA	16 V; Permanent
Current, max.	50 mA; Permanent
Cable length	000
• shielded, max.	200 m
shielded, max. Analog value generation for the inputs	
shielded, max. Analog value generation for the inputs Measurement principle	200 m Actual value encryption (successive approximation)
shielded, max. Analog value generation for the inputs Measurement principle Integration and conversion time/resolution per channel	Actual value encryption (successive approximation)
shielded, max. Analog value generation for the inputs Measurement principle	
shielded, max. Analog value generation for the inputs Measurement principle Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Integration time, parameterizable	Actual value encryption (successive approximation) 12 bit Yes; 16.6 / 20 ms
shielded, max. Analog value generation for the inputs Measurement principle Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Integration time, parameterizable Interference voltage suppression for interference	Actual value encryption (successive approximation) 12 bit
shielded, max. Analog value generation for the inputs Measurement principle Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Integration time, parameterizable Interference voltage suppression for interference frequency f1 in Hz	Actual value encryption (successive approximation) 12 bit Yes; 16.6 / 20 ms 50 / 60 Hz
shielded, max. Analog value generation for the inputs Measurement principle Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Integration time, parameterizable Interference voltage suppression for interference frequency f1 in Hz Time constant of the input filter	Actual value encryption (successive approximation) 12 bit Yes; 16.6 / 20 ms 50 / 60 Hz 0.38 ms
shielded, max. Analog value generation for the inputs Measurement principle Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Integration time, parameterizable Interference voltage suppression for interference frequency f1 in Hz Time constant of the input filter Basic execution time of the module (all channels)	Actual value encryption (successive approximation) 12 bit Yes; 16.6 / 20 ms 50 / 60 Hz
shielded, max. Analog value generation for the inputs Measurement principle Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Integration time, parameterizable Interference voltage suppression for interference frequency f1 in Hz Time constant of the input filter Basic execution time of the module (all channels released)	Actual value encryption (successive approximation) 12 bit Yes; 16.6 / 20 ms 50 / 60 Hz 0.38 ms
shielded, max. Analog value generation for the inputs Measurement principle Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Integration time, parameterizable Interference voltage suppression for interference frequency f1 in Hz Time constant of the input filter Basic execution time of the module (all channels released) Analog value generation for the outputs	Actual value encryption (successive approximation) 12 bit Yes; 16.6 / 20 ms 50 / 60 Hz 0.38 ms
shielded, max. Analog value generation for the inputs Measurement principle Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Integration time, parameterizable Interference voltage suppression for interference frequency f1 in Hz Time constant of the input filter Basic execution time of the module (all channels released) Analog value generation for the outputs Integration and conversion time/resolution per channel	Actual value encryption (successive approximation) 12 bit Yes; 16.6 / 20 ms 50 / 60 Hz 0.38 ms 1 ms
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shielded, max. Analog value generation for the inputs Measurement principle Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Integration time, parameterizable Interference voltage suppression for interference frequency f1 in Hz Time constant of the input filter Basic execution time of the module (all channels released) Analog value generation for the outputs Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Conversion time (per channel) Settling time for resistive load for capacitive load	Actual value encryption (successive approximation) 12 bit Yes; 16.6 / 20 ms 50 / 60 Hz 0.38 ms 1 ms 12 bit 1 ms 0.6 ms 1 ms
shielded, max. Analog value generation for the inputs Measurement principle Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Integration time, parameterizable Interference voltage suppression for interference frequency f1 in Hz Time constant of the input filter Basic execution time of the module (all channels released) Analog value generation for the outputs Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Conversion time (per channel) Settling time for resistive load for capacitive load for inductive load	Actual value encryption (successive approximation) 12 bit Yes; 16.6 / 20 ms 50 / 60 Hz 0.38 ms 1 ms 12 bit 1 ms 0.6 ms
shielded, max. Analog value generation for the inputs Measurement principle Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Integration time, parameterizable Interference voltage suppression for interference frequency f1 in Hz Time constant of the input filter Basic execution time of the module (all channels released) Analog value generation for the outputs Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Conversion time (per channel) Settling time for resistive load for capacitive load for inductive load Encoder	Actual value encryption (successive approximation) 12 bit Yes; 16.6 / 20 ms 50 / 60 Hz 0.38 ms 1 ms 12 bit 1 ms 0.6 ms 1 ms
shielded, max. Analog value generation for the inputs Measurement principle Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Integration time, parameterizable Interference voltage suppression for interference frequency f1 in Hz Time constant of the input filter Basic execution time of the module (all channels released) Analog value generation for the outputs Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Conversion time (per channel) Settling time for resistive load for capacitive load for inductive load Encoder Connection of signal encoders	Actual value encryption (successive approximation) 12 bit Yes; 16.6 / 20 ms 50 / 60 Hz 0.38 ms 1 ms 12 bit 1 ms 0.6 ms 1 ms 0.5 ms
shielded, max. Analog value generation for the inputs Measurement principle Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Integration time, parameterizable Interference voltage suppression for interference frequency f1 in Hz Time constant of the input filter Basic execution time of the module (all channels released) Analog value generation for the outputs Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Conversion time (per channel) Settling time for resistive load for capacitive load for inductive load Encoder	Actual value encryption (successive approximation) 12 bit Yes; 16.6 / 20 ms 50 / 60 Hz 0.38 ms 1 ms 12 bit 1 ms 0.6 ms 1 ms
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shielded, max. Analog value generation for the inputs Measurement principle Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Integration time, parameterizable Interference voltage suppression for interference frequency f1 in Hz Time constant of the input filter Basic execution time of the module (all channels released) Analog value generation for the outputs Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Conversion time (per channel) Settling time for resistive load for capacitive load for inductive load Encoder Connection of signal encoders for voltage measurement for current measurement as 2-wire transducer	Actual value encryption (successive approximation) 12 bit Yes; 16.6 / 20 ms 50 / 60 Hz 0.38 ms 1 ms 12 bit 1 ms 0.6 ms 1 ms 0.5 ms Yes Yes; with external supply
shielded, max. Analog value generation for the inputs Measurement principle Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Integration time, parameterizable Interference voltage suppression for interference frequency f1 in Hz Time constant of the input filter Basic execution time of the module (all channels released) Analog value generation for the outputs Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Conversion time (per channel) Settling time for resistive load for capacitive load for inductive load Encoder Connection of signal encoders for voltage measurement for current measurement as 2-wire transducer for current measurement as 4-wire transducer	Actual value encryption (successive approximation) 12 bit Yes; 16.6 / 20 ms 50 / 60 Hz 0.38 ms 1 ms 12 bit 1 ms 0.6 ms 1 ms 0.5 ms Yes Yes; with external supply Yes
shielded, max. Analog value generation for the inputs Measurement principle Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Integration time, parameterizable Interference voltage suppression for interference frequency f1 in Hz Time constant of the input filter Basic execution time of the module (all channels released) Analog value generation for the outputs Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Conversion time (per channel) Settling time for resistive load for capacitive load for inductive load Encoder Connection of signal encoders for voltage measurement for current measurement as 2-wire transducer for current measurement as 4-wire transducer for resistance measurement with two-wire connection	Actual value encryption (successive approximation) 12 bit Yes; 16.6 / 20 ms 50 / 60 Hz 0.38 ms 1 ms 12 bit 1 ms 0.6 ms 1 ms 0.5 ms Yes Yes; with external supply Yes Yes; Without compensation of the line resistances
shielded, max. Analog value generation for the inputs Measurement principle Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Integration time, parameterizable Interference voltage suppression for interference frequency f1 in Hz Time constant of the input filter Basic execution time of the module (all channels released) Analog value generation for the outputs Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Conversion time (per channel) Settling time for resistive load for capacitive load for inductive load Encoder Connection of signal encoders for outrent measurement for current measurement as 2-wire transducer for current measurement with two-wire connection for resistance measurement with three-wire connection	Actual value encryption (successive approximation) 12 bit Yes; 16.6 / 20 ms 50 / 60 Hz 0.38 ms 1 ms 12 bit 1 ms 0.6 ms 1 ms 0.5 ms Yes Yes; with external supply Yes Yes; Without compensation of the line resistances No
shielded, max. Analog value generation for the inputs Measurement principle Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Integration time, parameterizable Interference voltage suppression for interference frequency f1 in Hz Time constant of the input filter Basic execution time of the module (all channels released) Analog value generation for the outputs Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Conversion time (per channel) Settling time for resistive load for capacitive load for inductive load Fincoder Connection of signal encoders for outrent measurement for current measurement as 2-wire transducer for current measurement with two-wire connection for resistance measurement with three-wire connection for resistance measurement with four-wire connection	Actual value encryption (successive approximation) 12 bit Yes; 16.6 / 20 ms 50 / 60 Hz 0.38 ms 1 ms 12 bit 1 ms 0.6 ms 1 ms 0.5 ms Yes Yes; with external supply Yes Yes; Without compensation of the line resistances No
shielded, max. Analog value generation for the inputs Measurement principle Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Integration time, parameterizable Interference voltage suppression for interference frequency f1 in Hz Time constant of the input filter Basic execution time of the module (all channels released) Analog value generation for the outputs Integration and conversion time/resolution per channel Resolution with overrange (bit including sign), max. Conversion time (per channel) Settling time for resistive load for capacitive load for inductive load Encoder Connection of signal encoders for ourrent measurement for current measurement as 2-wire transducer for current measurement with two-wire connection for resistance measurement with two-wire connection for resistance measurement with four-wire connection for resistance measurement with four-wire connection Connectable encoders	Actual value encryption (successive approximation) 12 bit Yes; 16.6 / 20 ms 50 / 60 Hz 0.38 ms 1 ms 12 bit 1 ms 0.6 ms 1 ms 0.5 ms Yes Yes; with external supply Yes Yes; Without compensation of the line resistances No No

Errors/accuracies	
Temperature error (relative to input range), (+/-)	0.006 %/K
Crosstalk between the inputs, min.	60 dB
Repeat accuracy in steady state at 25 °C (relative to input range), (+/-)	0.06 %
Output ripple (relative to output range, bandwidth 0 to 50 kHz), (+/-)	0.1 %
Linearity error (relative to output range), (+/-)	0.15 %
Temperature error (relative to output range), (+/-)	0.01 %/K
Crosstalk between the outputs, min.	60 dB
Repeat accuracy in steady state at 25 °C (relative to output range), (+/-)	0.06 %
Operational error limit in overall temperature range	
 Voltage, relative to input range, (+/-) 	1 %
 Current, relative to input range, (+/-) 	1 %
 Resistance, relative to input range, (+/-) 	1 %
 Voltage, relative to output range, (+/-) 	1 %
 Current, relative to output range, (+/-) 	1 %
Basic error limit (operational limit at 25 °C)	
Voltage, relative to input range, (+/-)	0.8 %; Linearity error ±0.06 %
Current, relative to input range, (+/-)	0.8 %; Linearity error ±0.06 %
Resistance, relative to input range, (+/-)	0.8 %; Linearity error ±0.2 %
Resistance thermometer, relative to input range, (+/-)	0.8 %
 Voltage, relative to output range, (+/-) 	0.8 %
Current, relative to output range, (+/-)	0.8 %
Interference voltage suppression for f = n x (f1 +/- 1 %), f1 = interference	
Series mode interference (peak value of interference < rated value of input range), min.	30 dB
 Common mode interference, min. 	40 dB
Interfaces	
Number of industrial Ethernet interfaces	0
Number of PROFINET interfaces	0
Number of RS 485 interfaces	1; MPI
Number of RS 422 interfaces	1; RS 422 / 485 combined
Point-to-point connection	
Cable length, max.	1 200 m
Integrated protocol driver	
— 3964 (R)	Yes
— ASCII	Yes
— RK 512	Yes
Transmission rate, RS 422/485	
— with 3964 (R) protocol, max.	19.2 kbit/s; 38.4 kbit/s half duplex; 19.2 kbit/s full duplex
— with ASCII protocol, max.	19.2 kbit/s; 38.4 kbit/s half duplex; 19.2 kbit/s full duplex
— with RK 512 protocol, max.	19.2 kbit/s; 38.4 kbit/s half duplex; 19.2 kbit/s full duplex
1. Interface	
Interface type	Integrated RS 485 interface
Isolated	No
Interface types	
• RS 485	Yes
Output current of the interface, max.	200 mA
Protocols	
• MPI	Yes
PROFIBUS DP master	No
PROFIBUS DP device	No
Point-to-point connection	No
MPI	
Transmission rate, max.	187.5 kbit/s
Services	TOT TO KUNUS
	Vec
— PG/OP communication	Yes
— Routing	No Vos
— Global data communication	Yes
 — S7 basic communication 	Yes

— S7 communication	Voc. Only conver configured on one side
	Yes; Only server, configured on one side
— S7 communication, as client	No; but via CP and loadable FB
— S7 communication, as server	Yes
2. Interface	Interreted DO 400/405 interfere
Interface type	Integrated RS 422/ 485 interface
Isolated	Yes
Interface types	V DO 400 / 405 (V 07)
• RS 485	Yes; RS 422 / 485 (X.27)
Output current of the interface, max. Protocols	No
Protocols	Ma
MPI DESCRIPTION CONTACTION	No
PROFINET IO Controller	No
PROFINET ODA	No
PROFINET CBA PROFINED DB TOTAL	No
PROFIBUS DP master PROFIBUS DP davides	No
PROFIBUS DP device	No
Point-to-point connection	Yes
Point-to-point connection	40.01177 00.41177 1 17.1 1 17.1 17.1
Transmission rate, max.	19.2 kbit/s; 38.4 kbit/s half duplex; 19.2 kbit/s full duplex
Interface controllable from the user program	Yes
Interface can trigger alarm/interrupt in the user program	Yes; Message on break - identification
Protocols	
PROFIsafe	No
communication functions / header	
PG/OP communication	Yes
Data record routing	No
Global data communication	
• supported	Yes
 Number of GD loops, max. 	8
 Number of GD packets, max. 	8
 Number of GD packets, transmitter, max. 	8
 Number of GD packets, receiver, max. 	8
 Size of GD packets, max. 	22 byte
 Size of GD packet (of which consistent), max. 	22 byte
S7 basic communication	
supported	Yes
 User data per job, max. 	76 byte
 User data per job (of which consistent), max. 	76 byte; 76 bytes (with X_SEND or X_RCV); 64 bytes (with X_PUT or X_GET
	as server)
S7 communication	
• supported	Yes
• as server	Yes
• as client	Yes; Via CP and loadable FB
 User data per job, max. 	180 kbyte; With PUT/GET
User data per job (of which consistent), max.	240 byte; as server
S5 compatible communication	
• supported	Yes; via CP and loadable FC
Number of connections	
• overall	12
 usable for PG communication 	11
 reserved for PG communication 	1
 adjustable for PG communication, min. 	1
 adjustable for PG communication, max. 	11
 usable for OP communication 	11
 reserved for OP communication 	1
— adjustable for OP communication, min.	1
— adjustable for OP communication, max.	11
usable for S7 basic communication	8
 reserved for S7 basic communication 	0
 adjustable for S7 basic communication, min. 	0
adjustable for S7 basic communication, max.	8
•	

Number of login stations for message functions, max.	12; Depending on the configured connections for PG/OP and S7 basic
Trained of login stations for message fullotions, max.	communication
Process diagnostic messages	Yes
simultaneously active Alarm-S blocks, max.	300
est commissioning functions	
Status block	Yes; Up to 2 simultaneously
Single step	Yes
Number of breakpoints	4
Status/control	
 Status/control variable 	Yes
Variables	Inputs, outputs, memory bits, DB, times, counters
 Number of variables, max. 	30
of which status variables, max.	30
— of which control variables, max.	14
Forcing	
Forcing	Yes
Forcing, variables	Inputs, outputs
Number of variables, max.	10
Diagnostic buffer	
• present	Yes
 Number of entries, max. 	500
— adjustable	No
— of which powerfail-proof	100; Only the last 100 entries are retained
Number of entries readable in RUN, max.	499
— adjustable	Yes; From 10 to 499
— preset	10
Service data	
can be read out	Yes
nterrupts/diagnostics/status information	
Diagnostics indication LED	
Status indicator digital input (green)	Yes
Status indicator digital output (green)	Yes
ntegrated Functions	
Frequency measurement	Yes
Number of frequency meters	4; up to 60 kHz (see "Technological Functions" manual)
controlled positioning	Yes
integrated function blocks (closed-loop control)	Yes; PID controller (see "Technological Functions" manual)
PID controller	Yes
Number of pulse outputs	4; Pulse width modulation up to 2.5 kHz (see "Technological Functions" Manual)
Limit frequency (pulse)	2.5 kHz
otential separation	
Potential separation digital inputs	V
Potential separation digital inputs	Yes
between the channels	No V
between the channels and backplane bus Petertial acceptation digital subsults.	Yes
Potential separation digital outputs	Von
Potential separation digital outputs Potential separation digital outputs	Yes
between the channels between the channels in groups of	Yes
between the channels, in groups of between the channels and backglane bus	8 Vos
between the channels and backplane bus Potential separation analog inputs	Yes
Potential separation analog inputs	Vac: common for analog I/O
Potential separation analog inputs between the channels	Yes; common for analog I/O
between the channels between the channels and backplane bus	No Vec
between the channels and backplane bus Potential separation analog outputs	Yes
Potential separation analog outputs • Potential separation analog outputs	Yes; common for analog I/O
between the channels	No
between the channels between the channels and backplane bus	Yes
- between the chamiles and backplane bus	. 55

0°C
60 °C
Yes; STEP 7 V5.5 + SP1 or higher or STEP 7 V5.3 + SP2 or higher with HSP 203
No
see instruction list
8
see instruction list
see instruction list
Yes
Yes
Yes; With S7 block Privacy
120 mm
125 mm
130 mm
680 g

last modified:

